

## HE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Hong Jiang et al.

Art Unit:

2195

Serial No.:

10/750,589

Examiner:

Eric Charles Wai

Filed:

December 31, 2003

Docket:

ITL.1710US

P18028

For:

Visual and Graphical Data

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Assignee:

**Intel Corporation** 

Processing Using a Multi-Threaded Architecture

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **REPLY TO OFFICE ACTION**

Sir:

In response to the office action mailed November 30, 2007, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Pate of Deposit: February 18, 2008

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313,1450.

Cynthia L. Hayden